

**AMENDMENTS TO THE CLAIMS**

1. (Currently Amended) A liquid crystal display apparatus comprising:  
a transparent substrate;  
a plurality of first bus lines formed on said transparent substrate;  
a plurality of second bus lines formed on said transparent substrate  
substantially perpendicular to said first bus lines;  
a plurality of common electrode lines formed on said transparent  
substrate in parallel with said first bus lines;  
a plurality of pixels each connected to one of said first bus lines, one of  
said second bus lines and one of said common electrode lines; and  
a plurality of address marks formed on said transparent substrate, each  
of said address marks being adjacent to and connected to one of said first bus lines,  
said second bus lines and said common electrode lines.
2. (Original) The apparatus set forth in claim 1, wherein each of said  
address marks is provided at an end of said one of said first bus lines, said second bus  
lines and said common electrode lines.
3. (Original) The apparatus as set forth in claim 1, further comprising  
an insulating layer for covering said address marks.
4. (Original) The apparatus as set forth in claim 1, wherein said address  
marks comprise the same material as said one of said first bus lines, said second bus  
lines and said common electrode lines.

5. (Currently Amended) A liquid crystal display apparatus comprising:  
a transparent substrate;  
a plurality of gate bus lines formed on said transparent substrate;  
a plurality of signal bus lines formed on said transparent substrate  
substantially perpendicular to said gate bus lines;  
a plurality of common electrode lines formed on said transparent  
substrate in parallel with said gate bus lines;  
a plurality of pixels each connected to one of said gate bus lines, one of  
said signal bus lines and one of said common electrode lines; and  
a plurality of address marks formed on said transparent substrate, each  
of said address marks being adjacent to and connected to one of said gate bus lines.
6. (Original) The apparatus as set forth in claim 5, wherein each of said  
address marks is provided at an end of said one of said gate bus lines.
7. (Original) The apparatus as set forth in claim 5, further comprising  
an insulating layer for covering said address marks.
8. (Original) The apparatus as set forth in claim 5, wherein said address  
marks comprise the same material as said gate bus lines.
9. (Currently Amended) A liquid crystal display apparatus comprising:  
a transparent substrate;  
a plurality of gate bus lines formed on said transparent substrate;  
a plurality of signal bus lines formed on said transparent substrate  
substantially perpendicular to said gate bus lines;

a plurality of common electrode lines formed on said transparent substrate in parallel with said gate bus lines;

a plurality of pixels each connected to one of said gate bus lines, one of said signal bus lines and one of said common electrode lines; and

a plurality of address marks formed on said transparent substrate, each of said address marks being adjacent to and connected to one of said signal bus lines.

10. (Original) The apparatus as set forth in claim 9, wherein each of said address marks is provided at an end of said one of said signal bus lines.

11. (Original) The apparatus as set forth in claim 9, further comprising an insulating layer for covering said address marks.

12. (Original) The apparatus as set forth in claim 9, wherein said address marks comprise the same material as said signal bus lines.

13. (Currently Amended) A liquid crystal display apparatus comprising:  
a transparent substrate;  
a plurality of gate bus lines formed on said transparent substrate;  
a plurality of signal bus lines formed on said transparent substrate substantially perpendicular to said gate bus lines;  
a plurality of common electrode lines formed on said transparent substrate in parallel with said gate bus lines;  
a plurality of pixels each connected to one of said gate bus lines, one of said signal bus lines and one of said common electrode lines; and

a plurality of address marks formed on said transparent substrate, each of said address marks being adjacent to and connected to one of said common electrode lines.

14. (Original) The apparatus as set forth in claim 13, wherein each of said address marks is provided at an end of said one of said common electrode lines.

15. (Original) The apparatus as set forth in claim 13, further comprising an insulating layer for covering said address marks.

16. (Original) The apparatus as set forth in claim 13, wherein said address marks comprise the same material as said common electrode lines.